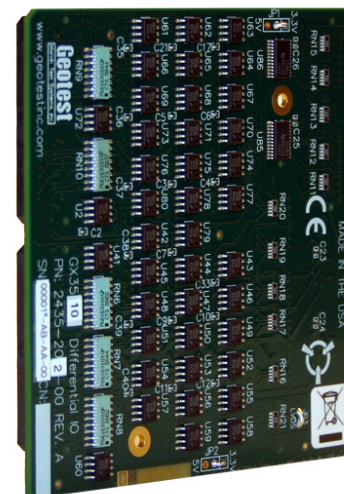


GX3510



80 CHANNEL MLVDS BUFFER EXPANSION BOARD FOR GX3500

- 80 channel, mLVDS expansion board for the GX3500 FPGA card
- Input / Output configurable on a per channel basis
- 200 Mb/s data rate



DESCRIPTION

The GX3510 is an 80 channel, mLVDS Buffer expansion board designed specifically for the GX3500 FLeXIO FPGA card.

SPECIFICATIONS

GX3510 MLVDS EXPANSION BOARD	
Number of Channels	80 I/O signals Direction is configurable by software on a per pin basis
Logic Family	M- LVDS. Type 2 receiver
Data Rate	200 Mbps (max)
Differential Output Voltage	480 mV (min), 650 mV (max)
Differential Input Voltage Threshold	150 mV (max) (positive going input) 50 mV (max) (negative going input)
Absolute Maximum Input Voltage Range	-1.8 V to 4 V
Power On State	All channels are configured as inputs

Note: Specifications are subject to change without notice

ORDERING INFORMATION

GX3510	FPGA Flex Daughtercard: 80 Channel Differential mLVDS
REQUIRED	
GX3500	FPGA PXI Digital I/O Card

GX3510



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DIGITAL I/O

